

FIG. 2

FIG. 3

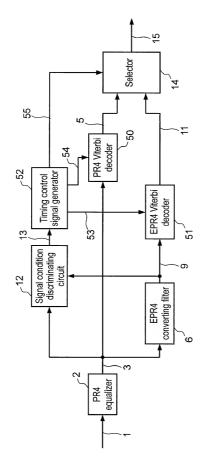
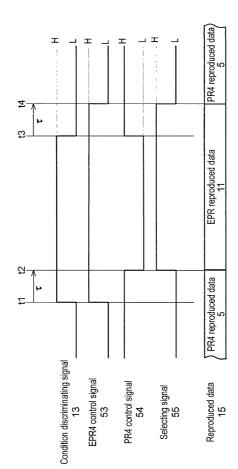


FIG. 4



Selector 5 EPR4 Viterbi PR4 Viterbi decoder decoder 90 Signal condition discriminating circuit equalizer PR4

12

EPR4 converting filter

Delay circuit

FIG. 5

FIG. 6

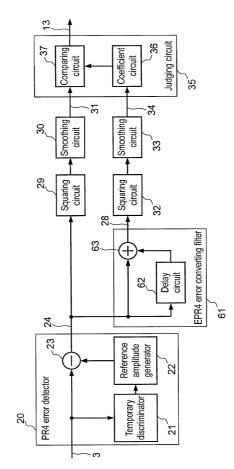


FIG. 7

